

Docket No.: MUH-12796

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MARIE DENISON ET AL.

Filed : CONCURRENTLY HEREWITH

Title : QUASI-VERTICAL SEMICONDUCTOR COMPONENT

INFORMATION DISCLOSURE STATEMENT

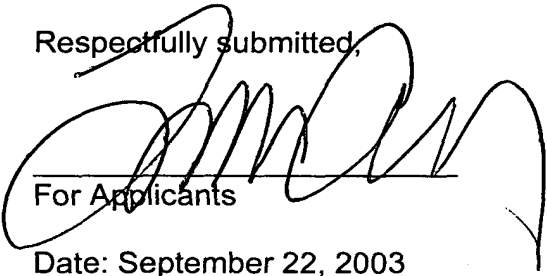
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36.

Respectfully submitted,

  
For Applicants

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Date: September 22, 2003

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<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: MUH-12796 Appl. No.:  Applicant: MARIE DENISON ET AL.  Filing Date: September 22, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
		Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36					
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			